

Notice of References Cited	Application/Control No. 10/712,288	Applicant(s)/Patent Under Reexamination CHAVES FILHO, ELISEU M.	
	Examiner Frank Duong	Art Unit 2666	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	I	US-			
	J	US-			
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	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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	U	Singh et al, MorphoSys: An Integrated Reconfigurable System for Data-Parallel and Computation-Intensive Applications, IEEE, pages 465-481, May 2000.
	V	Lee et al, Design and Implementation of the MorphoSys Reconfigurable Computing Processor, Journal of VLSI Signal Processing Systems, pages 1-38, 2000.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.